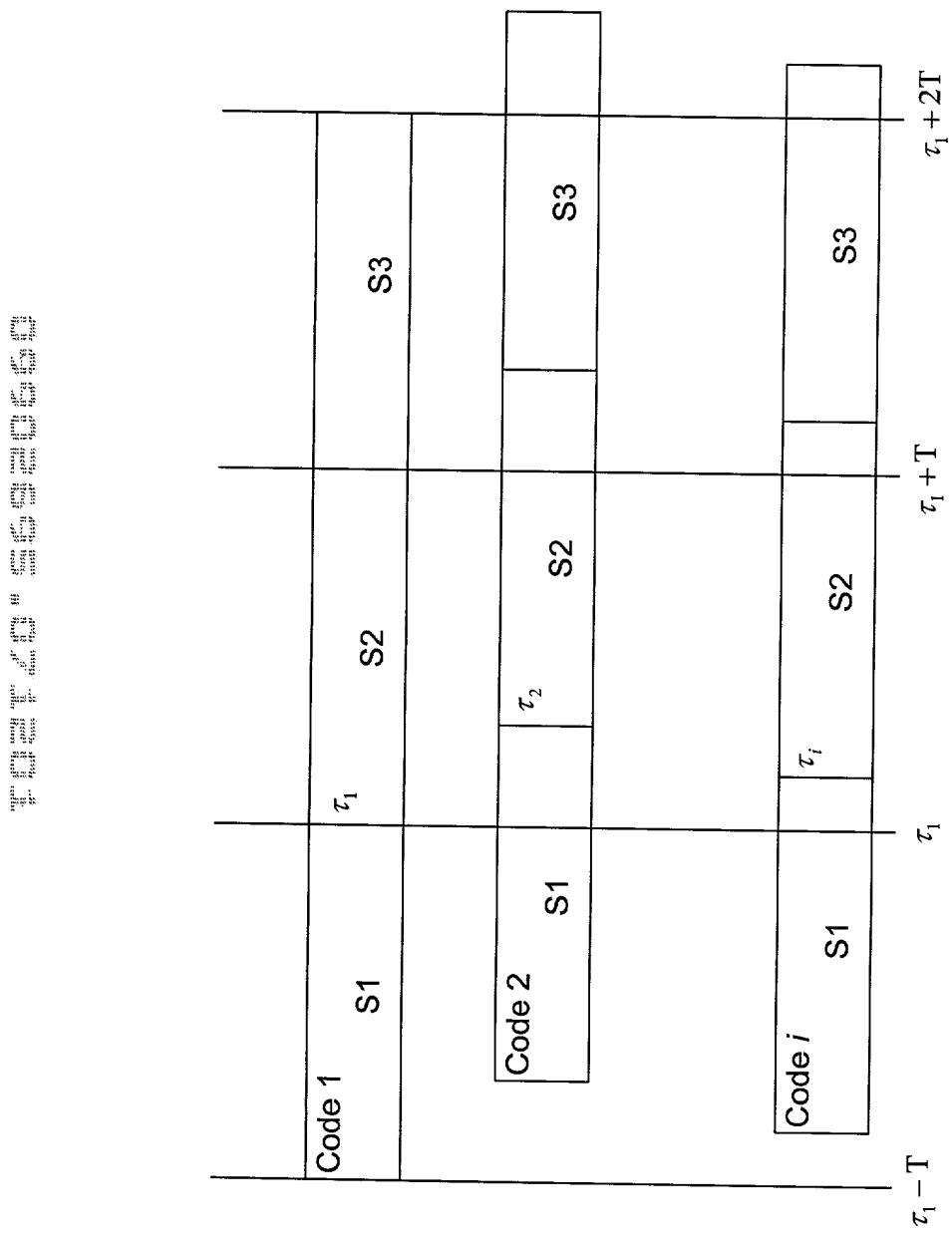


FIG. 1A

FIG. 1B



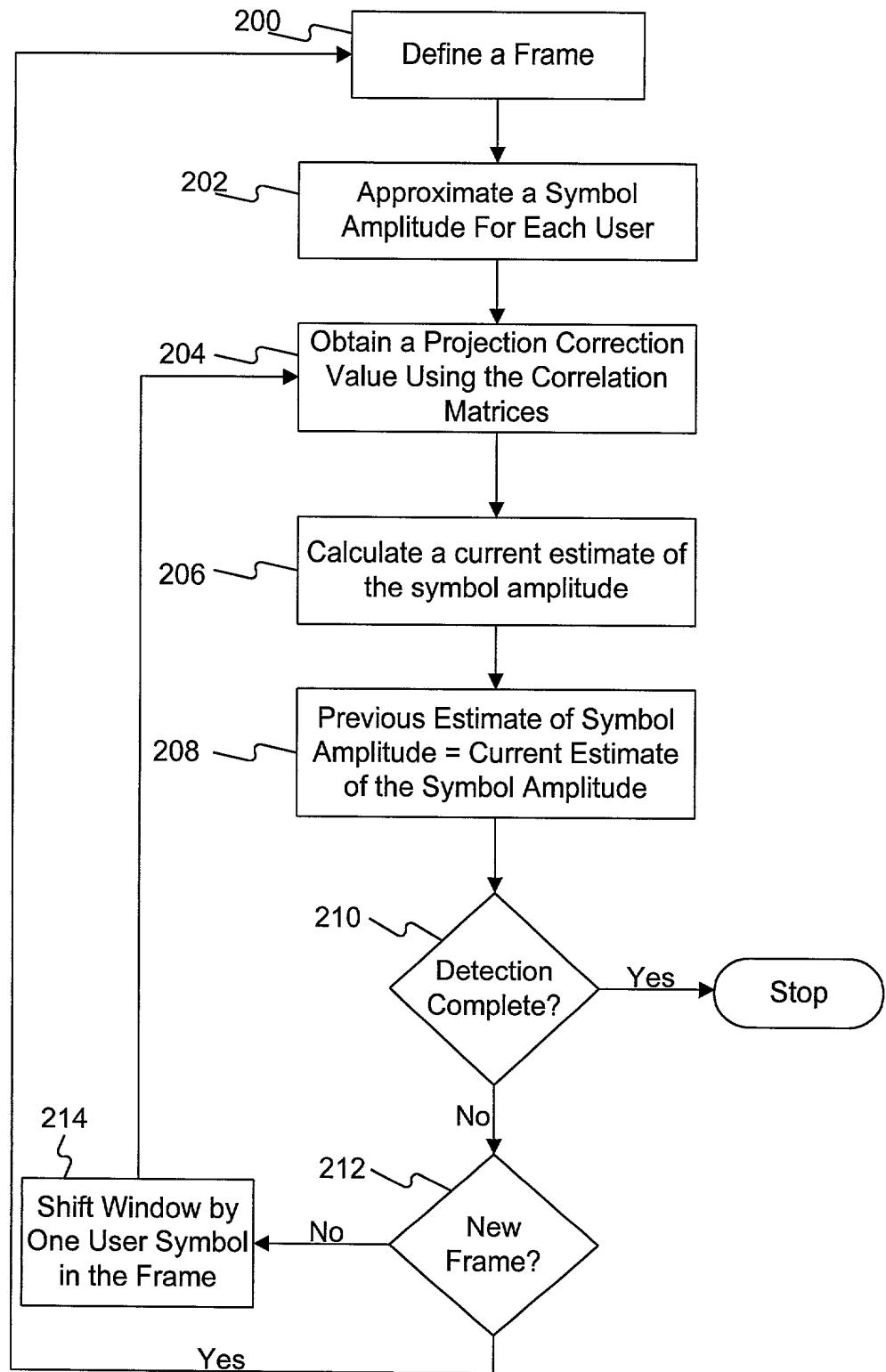


FIG. 2

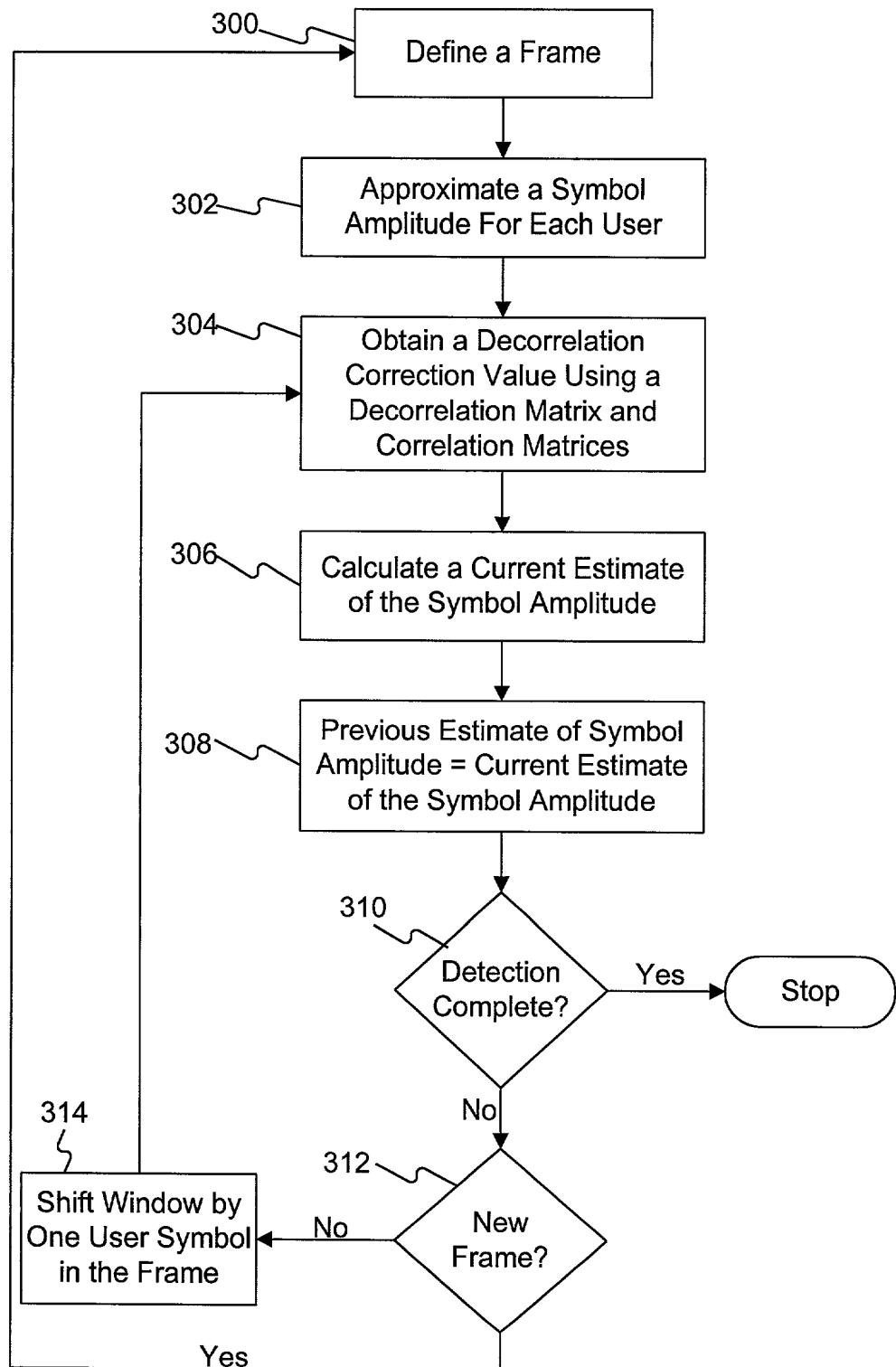


FIG. 3

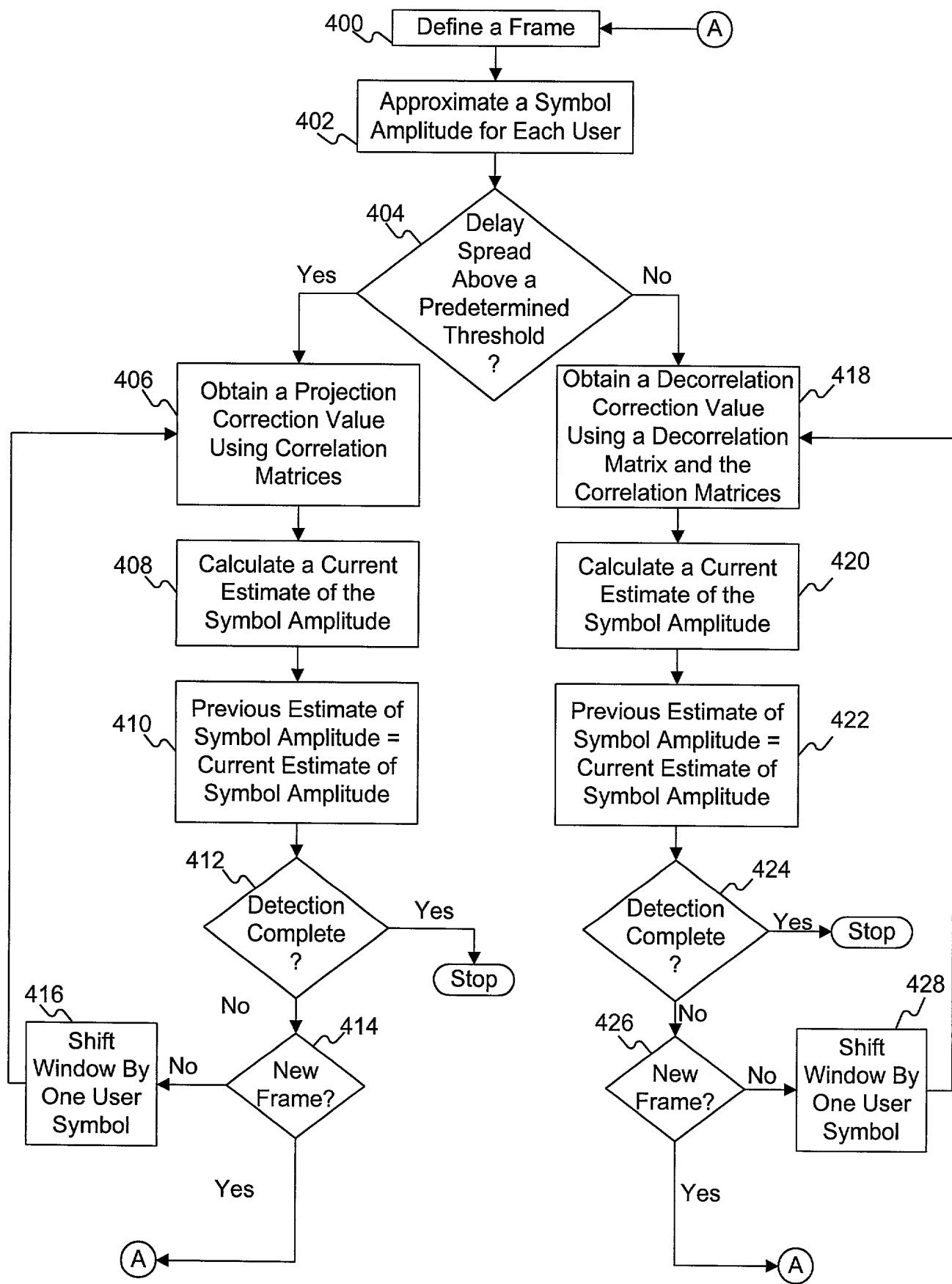


FIG. 4

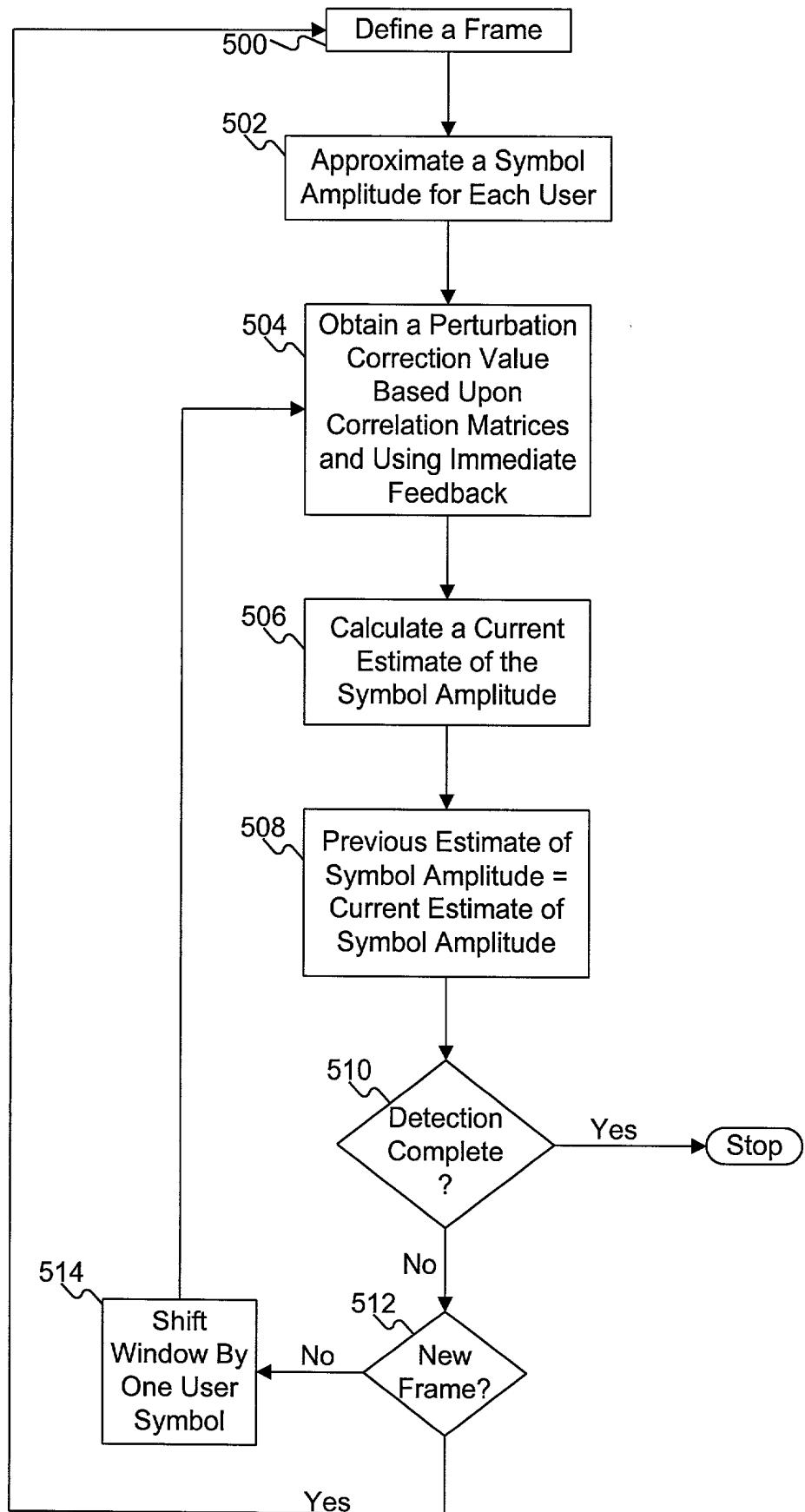


FIG. 5

FIG. 6

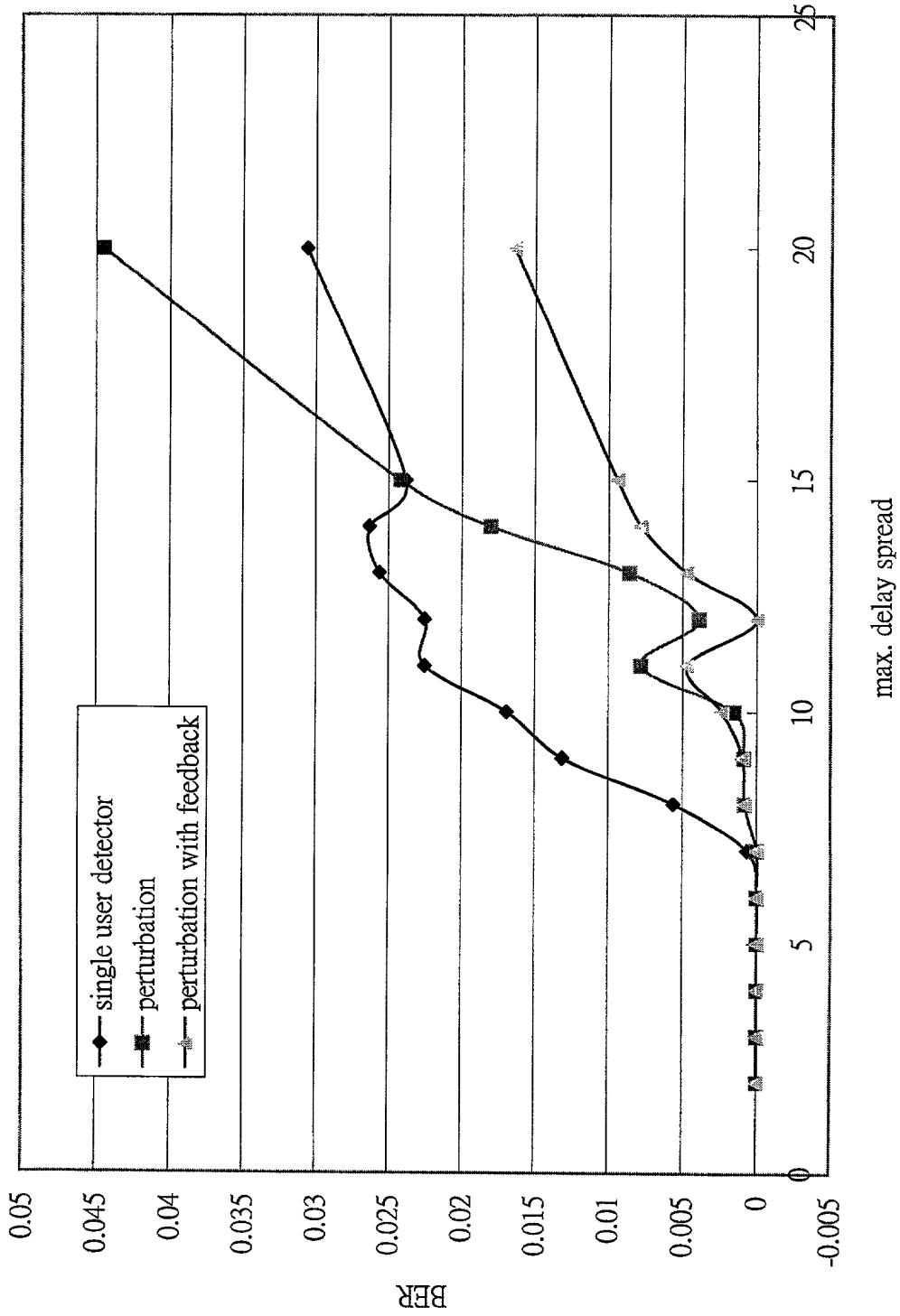


FIG. 7

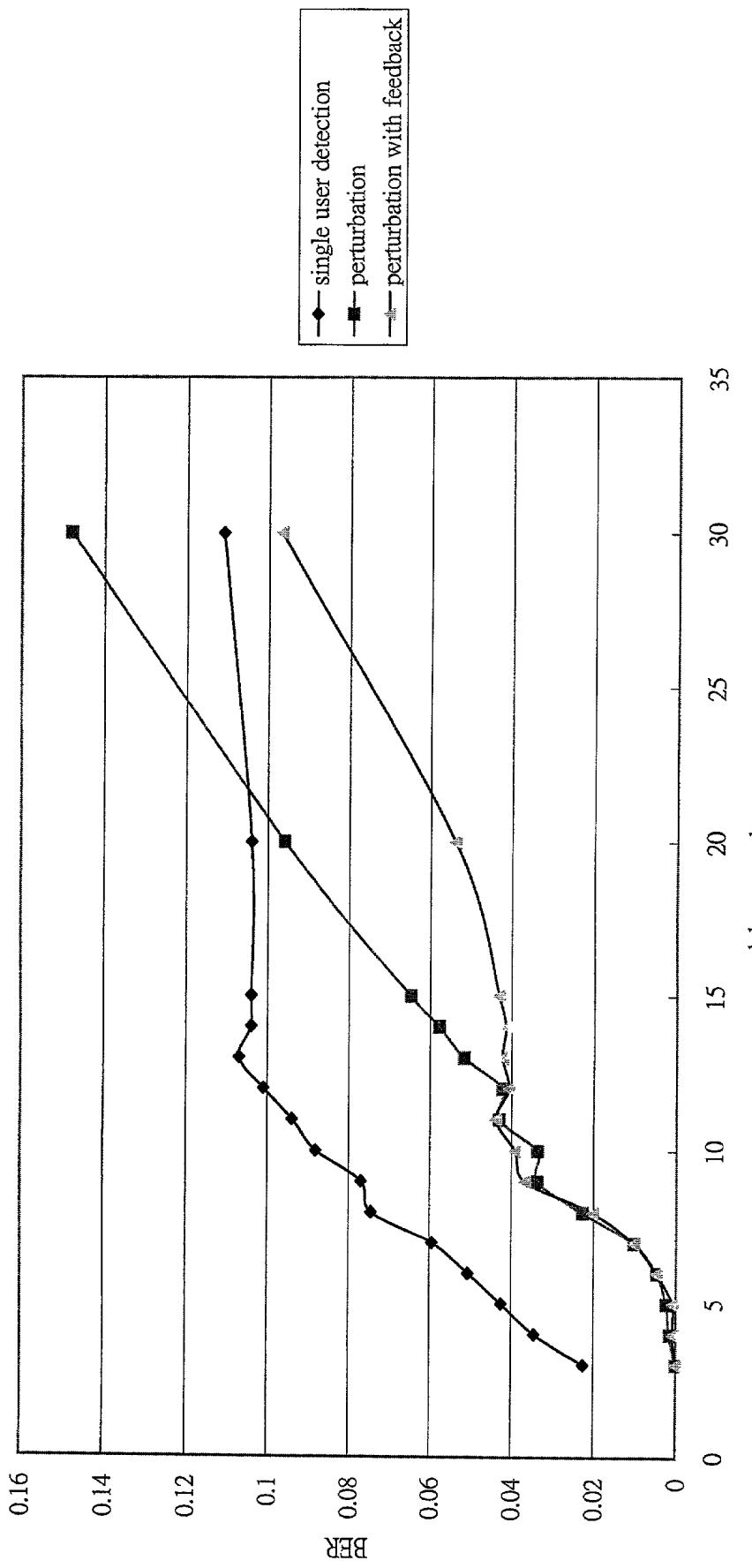


FIG. 8

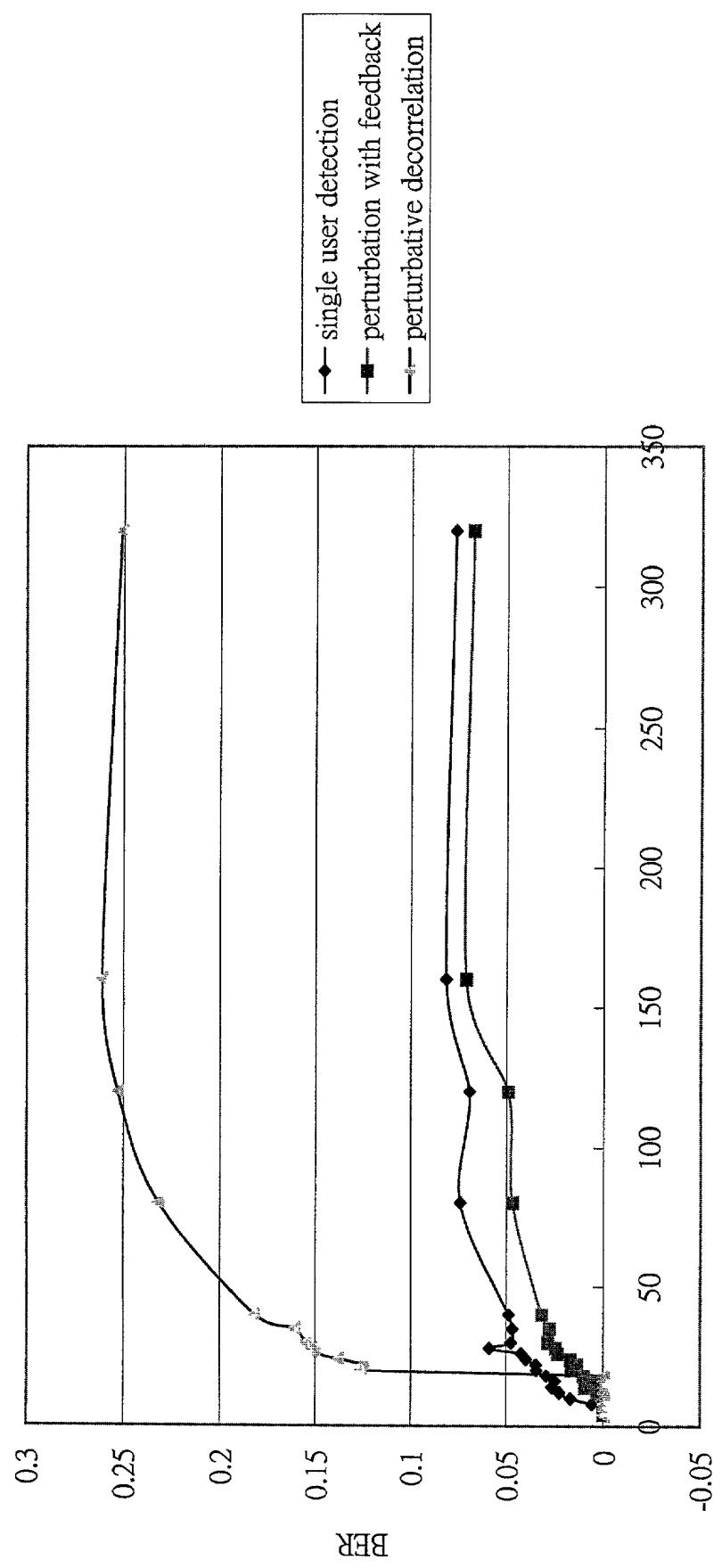
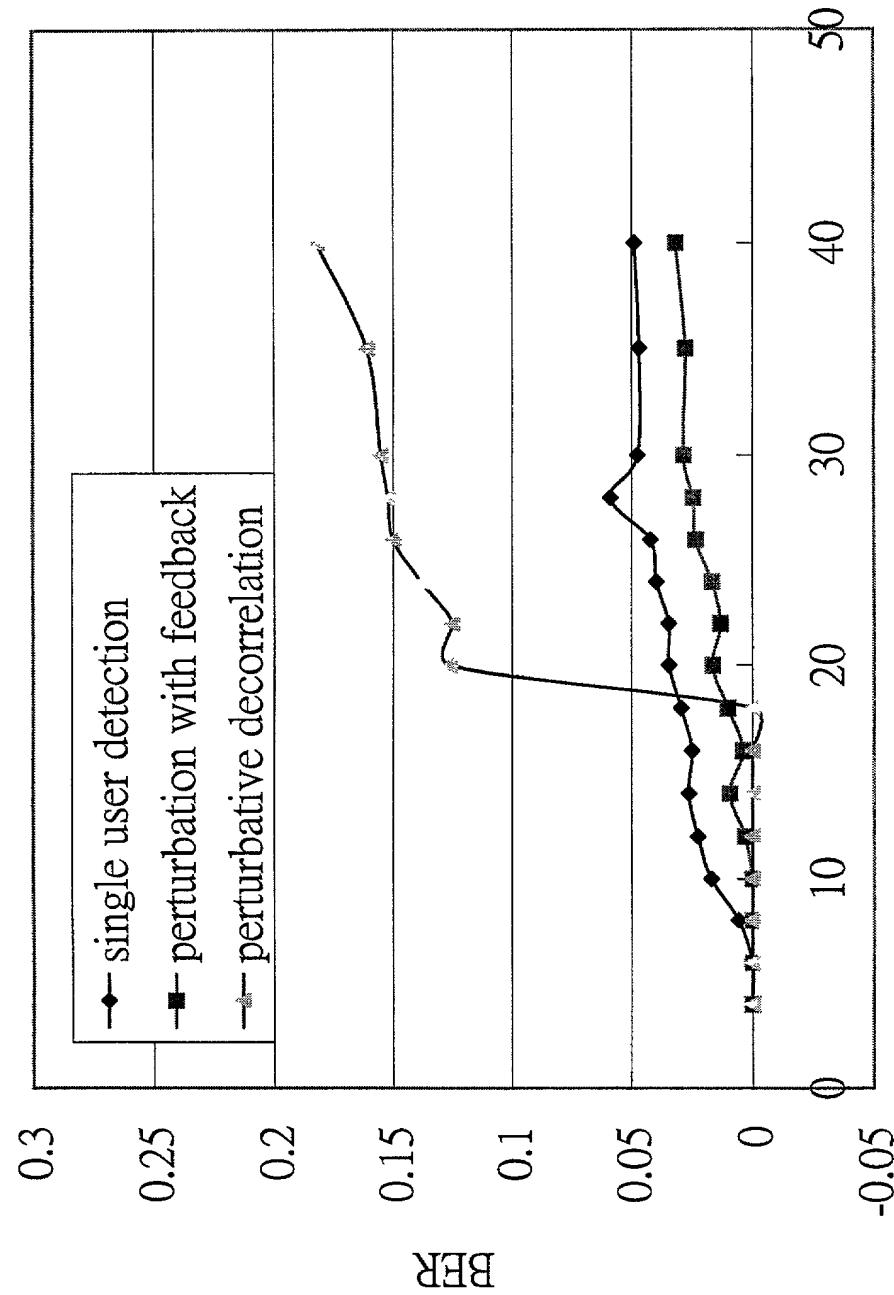


FIG. 9
max. delay spread



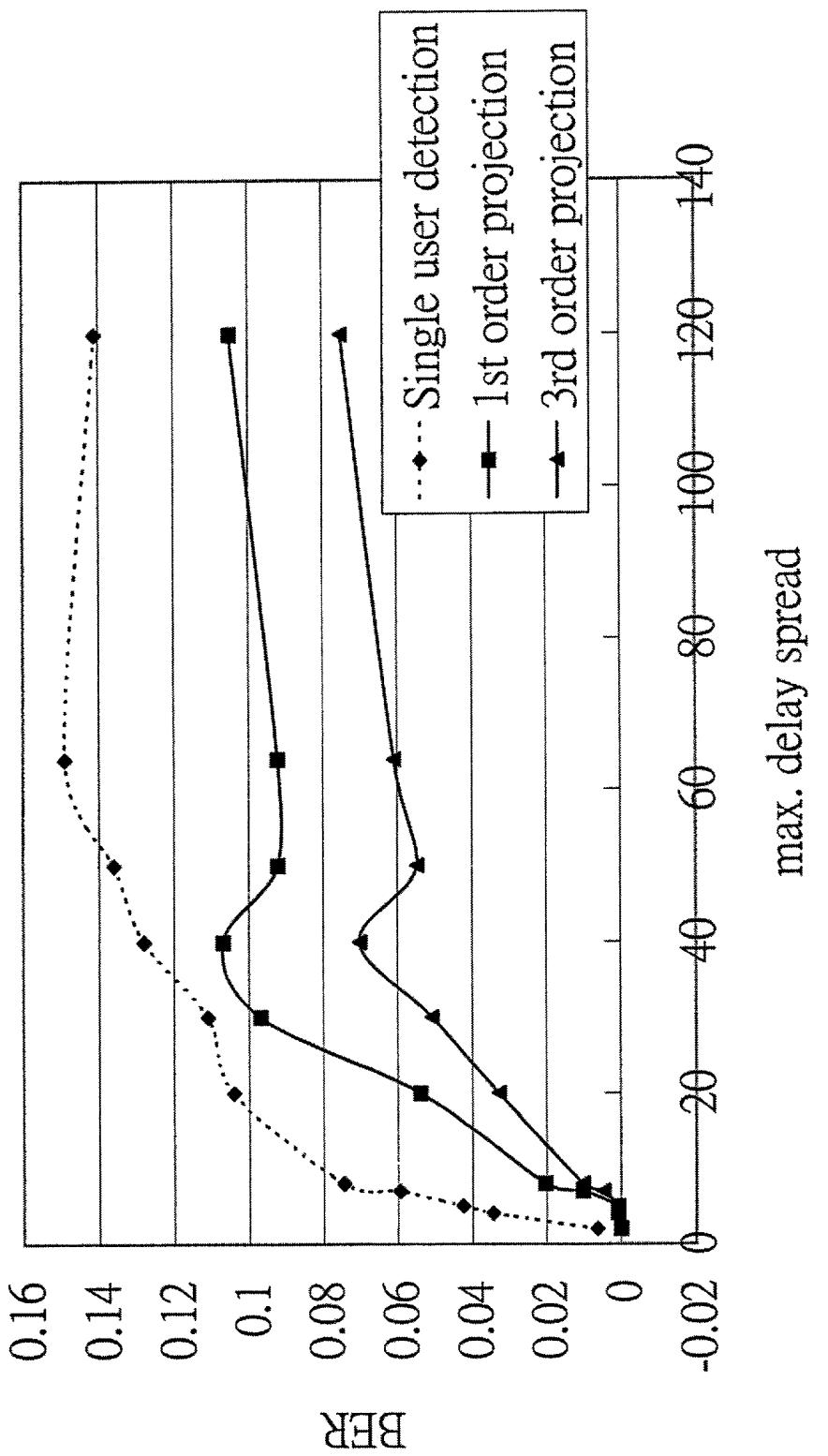


FIG. 10

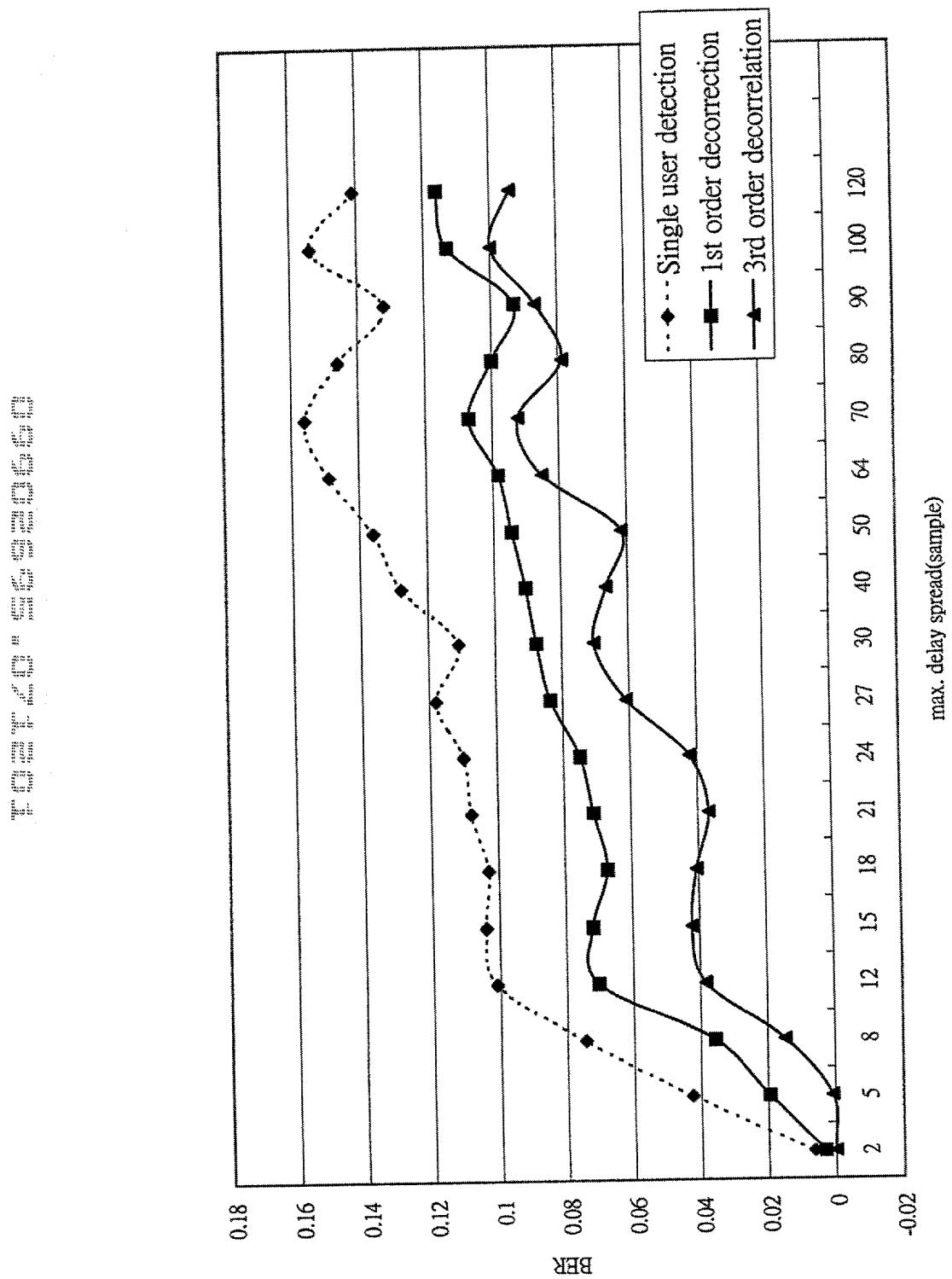


FIG. 11